Attorney Docket No. 5308-299

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re: Andrews et al. Confirmation: 9026 erial No.: 10/733,845 Group Art Unit: 2812

Filed: December 11, 2003

For: SEMICONDUCTOR SUBSTRATE ASSEMBLIES AND METHODS FOR

PREPARING AND DICING THE SAME

Date: May 5, 2004

Office of Patent Legal Administration Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

PETITION TO ACCORD THE PROPER FILING DATE

Sir:

As set forth in 37 CFR § 1.10(c) and MPEP § 506.02, Applicants hereby petition to grant the proper filing date of <u>December 11, 2003</u> rather than <u>December 12, 2003</u> for the above-identified patent application as this was the date the application was deposited as "Express Mail" in the U.S. Postal Service. The following is enclosed as evidence of <u>December 11, 2003</u> being the correct filing date for the application, which was properly filed under 37 C.F.R. § 1.10:

- 1. A true and accurate copy of the Cover Sheet for the Application showing the Express Mail signature and date on Page 2;
- 2. A true and accurate copy of the Express Mail Receipt along with a copy of the USPS-Track & Confirm from the web page (<u>www.usps.com</u>), showing "acceptance" or deposit date with the USPS at 5:35 p.m. on <u>December 11, 2003</u>; and
- 3. A true and accurate copy of our return post card and of the official filing receipt for this application, sent by the United States Patent and Trademark Office, showing that Applicants were improperly given the filing date of December 12, 2003 instead of the Express Mail date of December 11, 2003.

This petition is being filed promptly after Applicants became aware of the improperly accorded filing date. Accordingly, Applicants respectfully request the granting of December 11, 2003 as the filing date pursuant to 37 CFR § 1.10.

In re: Andrews et al. Serial No.: 10/733,845 Filed: December 11, 2003

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On the basis that Applicants properly followed the requirements of 37 CFR § 1.10 and the USPTO failed to accord the proper filing date under MPEP § 505 and thus the need to file this petition is due to error on the part of the USPTO, it is applicants' belief that no fee is due with this petition. However, the Commissioner is authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted,

David D. Beatty

Attorney For Applicants Registration No. 38,071

USPTO Customer No. 20792

Myers Bigel Sibley & Sajovec Post Office Box 37428

Raleigh, North Carolina 27627

Telephone: 919/854-1400 Facsimile: 919/854-1401

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Office of Patent Legal Administration, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 5, 2004.

Katie A. Chung



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ATTY DOCKET NO.: 5308-299 DATE: **December 11, 2003**

UTILITY PATENT APPLICATION TRANSMITTAL LETTER AND FEE TRANSMITTAL FORM (37 CFR § 1.53(b))

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450



P.O. Box 1450 Alexandria, VA 22313-1450	
Sir:	
Transmitted herewith for filing under 37 CFR § 1.53(b) is:	, the disclosure of which is
Inventor(s) or Application Identifier: Peter Andrews – Durham, NC Elbert Brown – Morrisville, NC Gerry Negley – Hillsborough, NC	
Entitled: SEMICONDUCTOR SUBSTRATE ASSEMBLIES AND MET DICING THE SAME	THODS FOR PREPARING AND
Myers Bigel Sibley & Sajovec, P.A. P. O. Box 37428 Raleigh, North Carolina 27627 Telephone: (919) 854-1400 Facsimile: (919) 854-1401 Customer No. 20792	
Enclosed are: 1. Application Transmittal Letter and Fee Transmittal Form (A duplic 2. 26 pages of Specification (including 59 claims) 3. 15 sheets of Informal Drawings (35 USC 113) 4. Oath or Declaration a. newly executed (original or copy) b. copy from prior application (37 CFR 1.63(d) (for continuation/division c. DELETION OF INVENTOR(S) (Signed statement deleting inventors) 5. Application Data Sheet. See 37 CFR 1.76 6. Incorporation By Reference (useable if box 4b is checked)	onal) [Note Box 6 Below] (s) named in the prior application)
The entire disclosure of the prior application, from which a copy of the oath or	declaration is supplied under

Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated
by reference therein.
7. Microfiche Computer Program (Appendix)
8. Assignment papers (cover sheet(s) and document(s))
9. Applicant claims small entity status. See 37 CFR § 1.27.
10. Information Disclosure Statement, PTO-1449, and 1 reference cited
11. Preliminary Amendment (Please enter all claim amendments prior to calculating the filing fee.)
12. English Translation Document
13. Certified Copy of Application No.; Filed
14. Sequence Listing/ Sequence Listing Diskette
a.
b. Daper copy
c. statement in support
15. An Associate Power of Attorney
16. Return Receipt Postcard (MPEP 503) (Should be specifically itemized)
17 Deletion of Inventors(s) (Signed statement attached deleting inventor(s) name in the prior application. See 37 CFR 1.63(d)(2) and 1.33(b)
18. Nonpublication Request under 35 U.S.C. 122(b)(2)(B)(i). Applicant must attach form.
19. Other: Application filed under 37 CFR 1.41(c)
The fee has been calculated as shown below:

	Column 1	Column 2	Small	Small Entity		Large Entity	
	No. Filed	No. Extra	Rate	Fee	Rate	Fee	
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INDEP CLAIMS	4- 3 =	1	x 43 = \$		x 86 = \$	86.00	
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		g fee is enclosed.	
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Respectfully submitted,

David D. Beatty

Registration No. 38,071

CERTIFICATE OF EXPRESS MAILING

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Katie A. Chung





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- ENROUTE, December 11, 2003, 6:37 pm, RALEIGH, NC 27623
- ACCEPTANCE, December 11, 2003, 5:35 pm, RALEIGH, NC 27612

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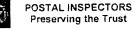
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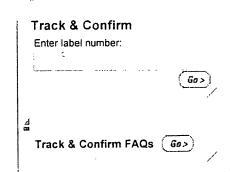
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5308-299 December 11, 2003

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Sir:

Kindly acknowledge receipt of the accompanying by placing your

receiving date stamp hereon and mailing:

Utility Patent Application Transmittal Letter and Fee Transmittal Form (in duplicate); Specification (26 pages – 59 claims); Drawings (15 sheets – Figures 1-15); Application Filed Under 1.41(c); Information Disclosure Statement; Form PTO-1449 (1 reference included); In re: Andrews et al.; Serial No: To be assigned; Filed: Concurrently herewith; For: SEMICONDUCTOR SUBSTRATE ASSEMBLIES AND METHODS FOR PREPARING AND DICING THE SAME.

Respectfully submitted,

MYERS BIGEL SIBLEY & SAJOVEC

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FILING OR 371 FIL FEE REC'D APPL NO. ART UNIT TOT CLMS ATTY.DOCKET NO DRAWINGS IND CLMS (c) DATE 0.00 10/733,845 12/12/2003 2812 5308-299 15 59 4 MYERS BIGEL Ε **CONFIRMATION NO. 9026** Myers Bigel Sibley & Sajovec, P.A. FILING RECEIPT P.O. Box 37428 Raleigh, NC 27627

OC000000012263239

Date Mailed: 04/05/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Peter Andrews, Durham, NC; Elbert Brown, Morrisville, NC; Gerry Negley, Hillsborough, NC;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 04/02/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

04-07-04 A98:47 IN

Title

Semiconductor substrate assemblies and method for preparing and dicing the same

Preliminary Class

438

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